LITHUANIAN COMPUTER SOCIETY

VILNIUS UNIVERSITY INSTITUTE OF DATA SCIENCE AND DIGITAL TECHNOLOGIES

LITHUANIAN ACADEMY OF SCIENCES



# 14th Conference on

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# Automated Verification of Railway Signalling Data

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SafeCap is a modern toolkit for modelling, simulation and formal verification of railway networks. This presentation discusses the use of SafeCap for formal analysis and automated, scalable safety verification of solid-state interlocking (SSI) programs – a technology at the heart of many railway signalling solutions around the world. The main driving force behind SafeCap development was to make it easy for signalling engineers to use the technology and thus to ensure its smooth industrial deployment. The unique qualities and the novelty of SafeCap are in making the use of formal notations and proofs fully transparent for the engineers. The presentation explains the formal foundations of the proposed method, its tool support, and its successful application by railway companies in developing industrial signalling projects.